EPI update

P. Notton / ETP4HPC presentation
SC2018

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EPI 2021 Moon Shot

US Apollo program (1961)

EPI program (2018)
EPI & E·S·T are the EuroHPC Industrial cornerstone
23 partners officially in the EPI consortium
EPI 23 partners, from research to industry from consortium to EU high tech fabless, enabling EPI ecosystem

EPI Common Platform PCI card
EPI HPC Blade
EPI Mother Board
EPI BMW Adas demo

Proof of concepts are 100% paid by EU and are made by the best experts in their domain

Fabless company
Industrial hand of EPI
Incorporated by a couple EPI members and external investors

1st EU H2020 program which is Product oriented !!!
EPI Common Platform

Key Markets

- Rhea family
- Automotive CPU
- HPC System Exascale
- Cronos family
- Automotive POC

HPC System

- PreExascale
- Rhea family
- 64 bits (*) core
- RISC-V

- Exascale
- Cronos family
- 64 bits (*) core

External IPs

- Few IPs Integration
- Some IPs
- Many IPs

EPI IP’s launch pad

Pan European Research Platform for HPC & AI

- 2021
- 2022
- 2023
- 2024

(* pending to final negotiations)
Zoom on EPI Common platform (Gen1)
Availability Q1/2021

- 64bits core General Purpose Processor (*)
- RISC-V Accelerator IP
- RISC-V HW root of trust
- RISC-V Power domain controller
- Embedded FPGA

- HPC System PreExascale
- Automotive CPU prototype
- Pan EU Research platform

More infos under NDA on a case by case basis

(* pending to final negotiations)
European Processor Initiative is all about this!!