



**EUROPEAN
TECHNOLOGY
PLATFORM
FOR HIGH
PERFORMANCE
COMPUTING**

Location: [Van der Valk Hotel](#), Sassenheim, Holland

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Day 0 - 12 Feb 2024		19:00	21:00	ETP4HPC Steering Board Dinner			
Day 1 - 13 Feb 2024		9:00	11:00	ETP4HPC Steering Board Meeting			
2024 ETP4HPC Conference							
		12:00	13:00	LUNCH			MODERATOR
		13:00	13:15	Opening & EUMaster4HPC - Introducing our Guests (EUMaster4HPC Students)	Jean-Pierre Panziera, ETP4HPC Chair & Gabriella Povero, ETP4HPC Office Head - with the participation of Amandine Samson (the EUMaster4HPC project/University of Sorbonne)		
	TECHNOLOGY VISION	13:15	14:00	KeyNote 1 - Trust in the Era of Data Deluge	Damien Gratadour, Observatoire de Paris, Centre national de la recherche scientifique		Michael Malms
	TECHNOLOGY VISION	14:00	14:15	Questions and Answers in relation to KeyNote 1			Michael Malms
	ROADMAPPING	14:15	14:45	Destination Earth's 'Federation' White Paper	Utz-Uwe Haus (HPE) & Craig Punty (SiPearl)		Michael Malms
	ETP4HPC 2.0 - Our Members' Voice	14:45	15:30	ETP4HPC 2.0 - Presentation of the new Vision for ETP4HPC (30' presentation +15' discussion)	Jean-Pierre Panziera, ETP4HPC Chair & Marcin Ostasz, ETP4HPC Office		Gabriella Povero
		15:30	16:00	CAFE			
	ETP4HPC 2.0 - Our Members' Voice	16:00	16:15	ETP4HPC 2.0 - Wrap-Up	Jean-Pierre Panziera, ETP4HPC Chair & Marcin Ostasz, ETP4HPC Office		
	NETWORKING	16:15	16:30	Presentation of New Members			Marcin Ostasz
	TECHNOLOGY VISION	16:30	17:00	KeyNote 2 - Long-term Computing Vision	Jean-Yves Berthou, INRIA		Daniele Cesarini
	TECHNOLOGY VISION	17:00	17:30	KeyNote 3 - A Vision of Industrial HPC Use - Challenges, Opportunities and Obstacles	Stephan Schenk, BASF		Daniele Cesarini
	NETWORKING	17:30	18:30	Disruptive Innovations - Giovanni Samaey (KU Luewen) - Time-X: time-parallel simulation tools in science and engineering & Manuel Arenza (Codee) - Automated code inspection for performance			Sai Narasimhamurthy
		18:30	18:45	Conclusions	Jean-Pierre Panziera, ETP4HPC Chair		
	NETWORKING	19:30	21:00	NETWORKING COCKTAIL DINNER			
Day 2 - 14 Feb 2024	ROADMAPPING	9:00	9:45	KeyNote 4 - SRA 6 - Main Research Directions, Interaction with the RIAG/EuroHPC JU/EC, Collaboration with other Continuum areas	Sai Narasimhamurthy, SRA 6 Leader		Gabriella Povero
	ROADMAPPING	9:45	10:45	Parallel Session - SRA 6 Workshop - HW Working Groups presenting their progress - Moderator: Michael Malms: System Architecture, System Hardware Components, Non-Conventional Architectures, I/O and Storage, Ecosystem Technologies, Quantum+HPC (Thematic Trend), Federation (Thematic Trend)	Parallel Session - SRA 6 Workshop - SW Working Groups presenting their progress - Moderator: Sai Narasimhamurthy: System Software and Management, Programming Environments, Maths and Algorithms, HPC Usage, Energy Efficiency (Thematic Trend), AI + Distributed Data Analytics (Thematic Trend)	With the participation of RIAG/INFRAG members, Moderated by: Michael Malms, ETP4HPC Office, Destination Earth Lead, SRA Lead and Sai Narasimhamurthy, SRA 6 Leader,	
		10:45	11:15	CAFE			
	ROADMAPPING	11:15	12:15	KeyNote 4 - EC & EuroHPC JU, followed by a discussion: Interaction ETP4HPC - RIAG - EuroHPC JU, HPC v. the rest of the Digital Continuum	Anders Jensen, EuroHPC Executive Director; Gustav Kalbe, EC, DG Connect, Head of HPC and Quantum Technology Unit	Moderators: Sai Narasimhamurthy, SRA 6 Leader, Jean-Philippe Nominé - RIAG Chair	Sai Narasimhamurthy
		12:15	12:30	Closure			Jean-Pierre Panziera & Gabriella Povero