

In this session Europe will present the results of its Exascale programme to date (covering the entire HPC system stack and application expertise), the ambitions of the new round of basic technology, application, co-design (DEEP-EST/EuroExa), prototype and processor design (EPI) projects, its post-Exascale plans, collaboration with Big Data, IoT and other areas, and a selection of best-performing HPC technology SMEs, emphasising Europe's global contributions.

EuroHPC, an initiative of EC and Member States, will provide the resources and mechanisms needed to integrate these efforts and build world-class Exascale machines.

More information on our website:



EUROPEAN
TECHNOLOGY
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Europe strikes back!

Birds-of-a-Feather session
Wednesday 14 November 2018
5:15 - 6:45 pm - Room D-166

Consolidating the European Exascale Effort

Programme

Welcome message

Jean-Pierre Panziera, ETP4HPC
Chairman

15 min EuroHPC – towards European world-class supercomputers

Juan Pelegrin, Head of Sector Exascale Computing, European Commission

10 min Achievements and Ambitions – an overview of the current European project landscape

François Bodin, EXDCI2 Scientific Director

European co-design projects:

5 min DEEP-EST

Estela Suarez (Jülich Supercomputing Centre)

5 min EuroExa

Peter Hopton (Iceotope)

5 min European Processor Initiative

Philippe Notton (Atos)

10 min European post-Exascale ambitions

Marcin Ostasz, ETP4HPC Office

10 min The achievements of European HPC technology SMEs

Iceotope (Peter Hopton)

Megware (Axel Auweter)

30 min Discussion with representatives of

- USA (Lori Diachin, ECP Deputy Director)

- Japan (Satoshi Matsuoka, RIKEN Director)

- China - TBC

European Exascale projects

2015/2016 Projects and Centres of Excellence



2018/2019 Projects and Centres of Excellence

