



EUROPEAN
TECHNOLOGY
PLATFORM
FOR HIGH
PERFORMANCE
COMPUTING

www.etp4hpc.
office@etp4hpc.

ETP4HPC NEWSLETTER

July - September 2017

Dear ETP4HPC Partners,

This third quarter of 2017, which included the summer holiday period, has been a key moment in ETP4HPC's activities. We collaborated closely with our 230+ working group members in compiling our third version of the Strategic Research Agenda ([SRA 3](#)). It has proven to be a very rich period in the life of our association as each issue of the SRA adds impetus and new collaborations to its activity. We would like to send a warm message of gratitude to everyone who contributed in this process.

We strongly believe this SRA will contribute to the academic and industrial competitiveness of Europe and we will release it in October, with its publication in hard copy following shortly. We are inviting you to participate in a 'Call for Comments' on this document and we greatly encourage you to share your thoughts on it, hence, facilitating the process in the future.

Also in the third quarter, ETP4HPC co-organised a [technical](#)

[exchange meeting](#) with Big Data Value Association (BDVA) with the view of improving a common technical language between their HPC and Big Data platforms and also in clarifying the perspective on common subjects such as compute infrastructure and the requirements for its next generation.

There were two European Extreme Data & Computing Initiative ([EXDCI](#)) events during this quarter. First, a [technical meeting](#) took place in July where a discussion on the current status of the EXDCI project and the preparations of its next round (EXDCI 2). The project's [final EXDCI conference](#) was held together with the ACM Europe conference in Barcelona early September.

Many of our members have been quite busy this summer preparing proposals for the 2017 calls of Work Programme 2016-2017 (WP16-17), especially the FETHPC calls. In particular, ETP4HPC submitted together with PRACE, a proposal for EXDCI 2, a follow-up of EXDCI Coordination and Support Action.

To conclude, we would like to invite you to join a [Birds-of-a-Feather \(BoF\)](#) session which we will be organising on behalf of EXDCI at [Supercomputing 17](#), Denver this November. The objective of this BoF is to showcase the most valuable contributions of the European HPC Technology and Applications Projects to the global HPC effort. Your presence there will be most welcome.

Jean-Pierre Panziera
ETP4HPC Chairman

.....



[HORIZON 2020 PROGRAMME FETHPC 2017 CALL CLOSURE](#)

The submission of proposals to the two topics under call [H2020-FETHPC-2017](#) closed on 26th of [September 2017](#). A total of seventy proposals were submitted in response to these topics... [\[+\]](#)



[PREPARATION FOR THE RELEASE OF SRA 3](#)

ETP4HPC is currently carrying out work on finalising its new Strategic Research Agenda (SRA 3), which is due to be released and distributed by the end of October 2017... [\[+\]](#)

[SRA 3 WORKING GROUP FINAL REVIEW MEETING, JULY 10, 2017, ZURICH](#)

This closed meeting took place at IBM, Zurich, where SRA working group leaders met to make a final review of the 3rd edition of the Strategic Research Agenda (SRA)... [\[+\]](#)

[ESD NETWORKING 2017](#)

If you are an external organisation and would like to contact organisations listed in a public copy of our Extreme-scale Demonstrators Networking 2017 form, please fill in this form... [\[+\]](#)



[2017 HANDBOOK OF EUROPEAN HPC PROJECTS](#)

We have finalised the 2017 Handbook - a professional publication, which will be distributed at our SC17 ...[\[+\]](#)



[ETP4HPC \(HPC\) & BDVA \(BIG DATA\) TECHNICAL EXCHANGE MEETING , JULY 4, 2017, BOLOGNA](#)

ETP4HPC co-organised a technical exchange meeting with Big Data Value Association (BDVA) with the view of improving a common technical language... [\[+\]](#)



[EXDCI TECHNICAL MEETING, JULY 3-4, 2017, BOLOGNA](#)

DAY 1 - a discussion on the current status of the EXDCI project and the preparations of its next round (EXDCI 2) ... [\[+\]](#)

[EXDCI FINAL CONFERENCE, SEPTEMBER 7-8, 2017, BARCELONA](#)

The EXDCI project final conference took place within the Annual ACM Europe Conference 2017 where there was a gathering of HPC specialists, students and ACM worldwide members.. [\[+\]](#)



[ETP4HPC MEMBERS INTERVIEWED BY PRIMEUR MAGAZINE \(EsD WORKSHOP
JUNE 22, 2017, FRANKFURT\)](#)

During a workshop organised by ETP4HPC, a number of ETP4HPC members were interviewed by Ad Emmen from Primeur Magazine...[\[+\]](#)



[NETWORKING COCKTAIL/DINNER, SEPTEMBER 6, 2017, BARCELONA](#)

This networking event took place in the heart of Barcelona. The objective was to gather ETP4HPC members, EXDCI experts and selected stakeholders in an informal networking environment during a cocktail/dinner...[\[+\]](#)



[NEW ETP4HPC MEMBERS](#)

Since the beginning of July we have five new members...[\[+\]](#)



[E-CAM \(CoE\) - EXASCALE STATE OF THE ART WORKSHOP, JULY 6-7, 2017, BARCELONA](#)

E-CAM - Centre of Excellence - Extreme-Scale State-of-the-Art Workshop took place on July 6-7, at CECAM-Es... [\[+\]](#)

[OPEN DIALOGUE EVENT, SEPTEMBER 6, 2017, BRUSSELS](#)

PPI4HPC organised an Open Dialogue Event (ODE), which took place on September 6 to inform the market of the future joint procurement and to gather... [\[+\]](#)



The banner for SC17 features a stylized logo of three interlocking rings in yellow, red, and black. To the right, the text "SC17" is prominently displayed in white, with "Denver, CO" and "hpc connects." below it. On the right side, there are logos for sponsors: ACM, sighpc, and IEEE Computer Society. Below the logos is a search bar with the text "Search this website ..." and a "Search" button. At the bottom right, the dates and location are listed: "Conference: November 12-17, 2017", "Exhibition: November 13-16, 2017", and "Colorado Convention Center - Denver, Colorado".

ETP4HPC on behalf of EXDCI will organise a **Birds-of-a-Feather (BoF) Session at**

Supercomputing 17

Denver, Colorado

to facilitate international collaboration between
the European HPC Ecosystem and overseas partners

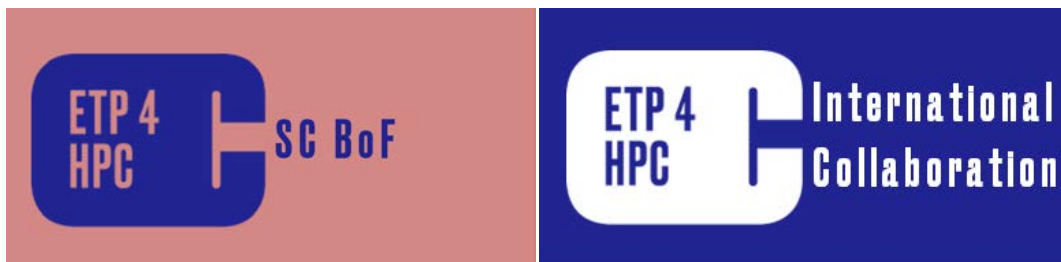
BoF session will take place in Room 501-502
from 5.15pm to 7pm
Tuesday November 14, 2017

Europe will present a holistic view of its HPC development and showcase its selected projects that contribute to the global exascale R&D effort.

The European HPC Technology and Application programme covers the entire HPC system stack and application expertise; including system prototypes (see www.etp4hpc.eu/euexascale for a full description).

We will summarize our ambitions and the contribution of our projects, present selected technology ([Mont-Blanc](#), [SAGE](#)) and application projects ([BioExcel](#)) and discuss our role in the global HPC effort. The European EXDCI (www.exdci.eu) booth, publications ([European HPC Handbook](#)) and the [ETP4HPC website](#) will facilitate networking before and after the BoF.

- Consult our [BoF web page](#) (it includes the BoF programme).
- Download our BoF Flyer [here](#) (it includes the BoF programme).
- Consult our BoF Programme on the SC site [here](#).
- Download our [European HPC Handbook](#) to be distributed at the BoF.



[2ND JPI OCEANS CONFERENCE](#)

October 26, 2017

LISBON, PORTUGAL [\[+\]](#)

[ICT PROPOSER'S DAY 2017](#)

November 9-10, 2017

BUDAPEST, HUNGARY [\[+\]](#)

[HIGH-LEVEL GOVERNANCE
MEETING OF THE EUROPEAN
PLATFORM OF NATIONAL
INITIATIVES ON DIGITISING
INDUSTRY](#)

November 21, 2017

BUSSELS, BELGIUM [\[+\]](#)

[BIOEXCEL COMMUNITY FORUM](#)

November 22-23, 2017

AMSTERDAM, NETHERLANDS [\[+\]](#)

[INTERNATIONAL CAE CONFERENCE](#)

November 6-7, 2017

VICENZA, ITALY [\[+\]](#)

[BDVA AND ETP4HPC WORKSHOP](#)

November 20, 2017

VERSAILLES, FRANCE [\[+\]](#)

[EUROPEAN BIG DATA VALUE
FORUM](#)

November 21-23, 2017

VERSAILLES, FRANCE [\[+\]](#)

[THE EUROPEAN FORUM FOR
ELECTRONIC COMPONENTS AND
SYSTEMS](#)

December 5-7, 2017

BRUSSELS, BELGIUM [\[+\]](#)