

Note:

The objective of this Newsletter is to provide a regular update on the current activities of ETP4HPC.

Your contact details have been added to this list as you have asked to subscribe to our Newsletter or have been in touch with ETP4HPC. If you wish to unsubscribe, please let us know at office@etp4hpc.eu. Thank you for your understanding.



This Newsletter is also available at:
www.etp4hpc.eu/en/newsletters.html

ETP4HPC Newsletter – Jan – Mar 2016

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General Assembly of March 15, 2016, Barcelona

ETP4HPC held its sixth General Assembly on 15th March 2016. Barcelona Supercomputing Center hosted this event. Invited speakers were Dr. Panagiotis Tsarchopoulos, EC/DGCNECT, with an update from the European Commission, and, Prof. Mateo Valero, Director of Barcelona Supercomputing Center and a Cray Seymour Awardee, who presented his view of the development of the European HPC industry.

Activity and financial report of the association for 2015 were presented, as well next SRA actions – following the 2015 update of our roadmap, international cooperation actions and impact assessment and related monitoring of the ecosystem. New ETP4HPC members were given the opportunity to introduce themselves, before a presentation of 2016 perspectives and a discussion session that closed the day.

To find out more: <http://www.etp4hpc.eu/news/etp4hpc-general-assembly-march-2016/>

New ETP4HPC Members

ETP4HPC currently has 72 members

The following organisations have joined ETP4HPC since January 2016:

Full member: *Delft University of Technology*

Associated member: *CybeleTech, ONERA*



To find out more visit: <http://www.etp4hpc.eu/en/membership.html>

Office expansion

At the beginning of February Maïke Gilliot joined our office team, based in France on the Campus Teratec. She will contribute to general office tasks and will be, in particular, in charge of the coordination of ETP4HPC's participation in EXDCI. Furthermore, she will contribute to the activities related to innovation and support for SMEs and Start-ups of the HPC ecosystem.

Strategic Research Agenda and Public Call for Comments

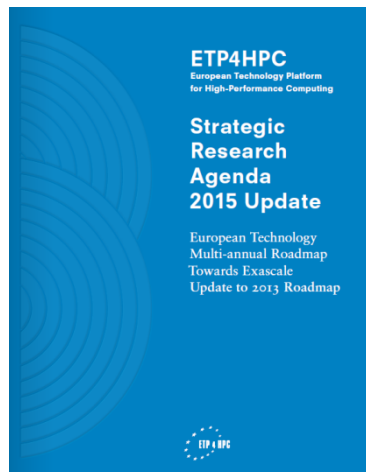
The updated Strategic Research Agenda (SRA 2) of ETP4HPC, which was published last November, printed version is now available at the following location:

<http://www.etp4hpc.eu/sra>

Also on Digital4Science EU platform:

<https://ec.europa.eu/futurium/en/digital4science/discussions/ETP4HPC>

This document serves as a technical reference for FETHPC calls in Work Programme 2016-2017 of Horizon 2020 (see below).



ETP4HPC and the European Commission would like to receive feedback on this document from the HPC community. In this process, any organisation or individual is able to express their view on this document. We will use the comments collected in order to prepare the next full version of our SRA which is due to be published in July 2017 (the current document will not be amended). This call for comments will close when we start preparing the next SRA, i.e. the end of June 2016.

In order to submit your comments, please use the following form:

<https://www.surveymonkey.com/r/ETP4HPC-SRA2-PUBLIC-CALL4COMMENTS>

If you wish to comment on specific parts of the document, please request an editable copy of it from ETP4HPC office by sending an email to: office@etp4hpc.eu

ETP4HPC 2015 SRA: <http://www.etp4hpc.eu/sra>

FETHPC 2016/17 Call

The HPC contents of Work Programme 2016-2017 have been published. ETP4HPC has worked with the European Commission on the research topics for this call, within the framework of HPC contractual Public Private Partnership.

The call has 3 topics:

- FETHPC-01-2016: Co-design of HPC systems and applications, for RIA (Research and Innovation action) projects, closing September 2016 with a budget of €41 M.
- FETHPC-02-2017: Transition to Exascale Computing, for RIA projects, closing September 2017 with a budget of €40 M.
- FETHPC-03-2017: Exascale HPC ecosystem development, for CSA (Coordination and Support Action) projects, closing September 2017 with a budget of €4 M.

Full text of the call can be read at:

<http://ec.europa.eu/research/participants/portal/desktop/en/opportunities/h2020/calls/h2020-fethpc-2016-2017.html>

http://ec.europa.eu/research/participants/data/ref/h2020/wp/2016_2017/main/h2020-wp1617-fet_en.pdf

EXDCI



The European Extreme Data & Computing Initiative (EXDCI) objective is to coordinate the development and implementation of a common strategy for the European HPC Ecosystem. EXDCI website now gives more details on the project activities: technology and application road mapping (the former being closely related to ETP4HPC's SRA), talent generation, impact assessment, international collaboration...

Read out more at: <https://exdci.eu/>

New work groups

In addition to SRA efforts which deal with upfront R&D topics and priorities definition, ETP4HPC is launching 2 new work groups that aim at increasing the impact of R&D projects outcomes and strengthening the links with operational deployment of systems and software:

- ‘Energy Efficiency’ will be coordinated by LRZ
This group will promote energy-efficient design and use of HPC systems and infrastructures, liaising with the international Energy Efficient HPC Working Group, establishing and promoting methods and metrics to assess energy efficiency at infrastructure, system and node levels; promoting tools, means to monitor, and optimise energy efficient operation
- ‘HPC Software’ will be coordinated by EPCC
The key motivation for the HPC Software WG is to increase the long-term impact of software that is developed in the European Union, e.g. as part of FP7 and H2020 projects. In this context, software is not limited to scientific applications, but includes system level software and libraries (often called ‘systemware’).

ETP4HPC new website, ETP4HPC social media

A revamped website has been deployed during the last week of March.



ABOUT US STRATEGIC RESEARCH AGENDA EUROPEAN HPC MEMBERS PUBLICATIONS
NEWS EVENTS NETWORKING SRA WORKSHOP AT ISC'16
ESD WORKSHOP AT HPC SUMMIT WEEK EUROPEAN EXA-SCALE PROJECTS

SEARCH



MEMBER AREA

**The European Technology Platform for
High Performance Computing**



<https://twitter.com/etp4h>



<https://www.linkedin.com/company/etp4hpc-european-technology-platform-for-hpc>

Recent events

HiPEAC's 11th Conference Prague, Czech Republic, January 18-20, 2016



2016 Conference
January 18-20, 2016, Prague, Czech Republic

HiPEAC's Conference took place from January 18 to 20, 2016, Prague, Czech Republic

ETP4HPC's chairman, Jean-François Lavignon, was an invited speaker at HiPEAC's 11th Conference, which took place over three days and attracted over 600 delegates this year.

ORAP Forum

The 37th ORAP Forum took place in Paris March 17th with more than 100 participants.



Jean-François Lavignon, ETP4HPC chairman, gave a talk in which he represented the landscape of HPC technology research in Europe – first pillar. Presentation summaries and slides are available at: http://orap.irisa.fr/?page_id=215

think.BDPST Conference, March 8-10, 2016, Budapest



ETP4HPC's, Marcin Ostasz, SRA Expert, was one of the speakers on the eleven panels of the three-day conference.

Link to this news at: <http://www.etp4hpc.eu/en/news.html>

Upcoming events and activities

♦ [Computing System Week](#)



Porto, April 20th to 22nd, 2016

ETP4HPC will represent EXDCI at the 2016 spring edition of the Computing System Week (CSW).

♦ [European HPC Summit Week](#)



Prague, May 9th to 12th, 2016

ETP4HPC will participate in the HPC Summit Week (9-12 May 2016, Prague), an event organised with the support of [EXDCI](#).

We will be facilitating a workshop on Extreme-Scale Demonstrators (Thu, May 12), and participating in EXDCI general events with FETHPC projects and CoEs (May 9 and 10).

<https://exdci.eu/events/hpc-summit-week-exdci-workshop>

♦ [International Supercomputing Conference, ISC](#)

Frankfurt, June 19th to 23rd, 2016



We will be present at this event with a booth in the exhibition. ETP4HPC is also planning on organising a half-day workshop to discuss our SRA.

♦ [Teratec 2016 Forum](#)



Palaiseau, June 28th to 29th, 2016

ETP4HPC will be participating in this forum at [Booth No. 10](#).

♦ [SC16, Supercomputing 2016](#)



Salt Lake City, November 13th to 18th, 2016

ETP4HPC will attend SC16, Supercomputing 2016 with the objective of continuing the presentation of the European HPC Technology Eco-system.

♦ [ECMI Conference 2016](#)



Santiago de Compostela, June 13th to 17th, 2016

ETP4HPC will have a representative at this 19th edition of the European Conference on Mathematics for Industry.

Link to these events at: <http://www.etp4hpc.eu/en/events.html>

Other news and links

- **FETHPC project ESCAPE 1st Dissemination and Training workshop** between the 18th and 20th October 2016 in Elsinore, Denmark on the topic of: "Energy-efficient Scalable Algorithms for Weather- and Climate prediction: Status and Prospects".
<http://www.hpc-escape.eu/media-hub/escape-events/1st-dissemination-workshop>
- **ETP4HPC article in Pan European Network Publications**
The 18th March issue of Pan European Network Publications features an article by Jean-François Lavignon, outlining how our association is helping to advance the HPC agenda in Europe.
<http://www.paneuropeannetworkpublications.com/ST18/#86>
- **Open consultation on Mathematics for Horizon 2020 next Work Programme**
The European Commission has opened the following call for comments 'Open consultation on Mathematics for Horizon 2020 next Work Programme' which is open until the 30th of April. We invite you to take this opportunity to contribute:
<https://ec.europa.eu/futurium/en/content/online-consultation-mathematics-excellence-science-horizon2020>

The ETP4HPC has already submitted a contribution, referring to the corresponding chapter of our [SRA](#) to this consultation process.

- ***Information and Networking Days on Horizon 2020 Big Data Topics, Brussels***
The Data Value Chain Unit of the European Commission organised an [Information and Networking Days on Horizon 2020 Big Data topics](#), January 14-15, 2016, Brussels.
- ***Horizon 2020 EC web pages on HPC***
<https://ec.europa.eu/programmes/horizon2020/en/h2020-section/high-performance-computing-hpc>