

Towards European HPC Systems fulfilling the requirements of Big Data



ETP4HPC SRA Team

4th closed BDEC Workshop







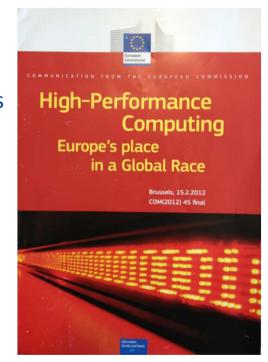
What you should know by the end of this talk?

- The European HPC Eco-system & the role of the European HPC Technology Platform (ETP4HPC)
- Strategic Research Agenda (SRA) the European HPC Technology Roadmap
- The European HPC Technology calls and projects
- Prototyping the European Extreme-Scale Demonstrators and their Big Data related characteristics
- How could we collaborate?

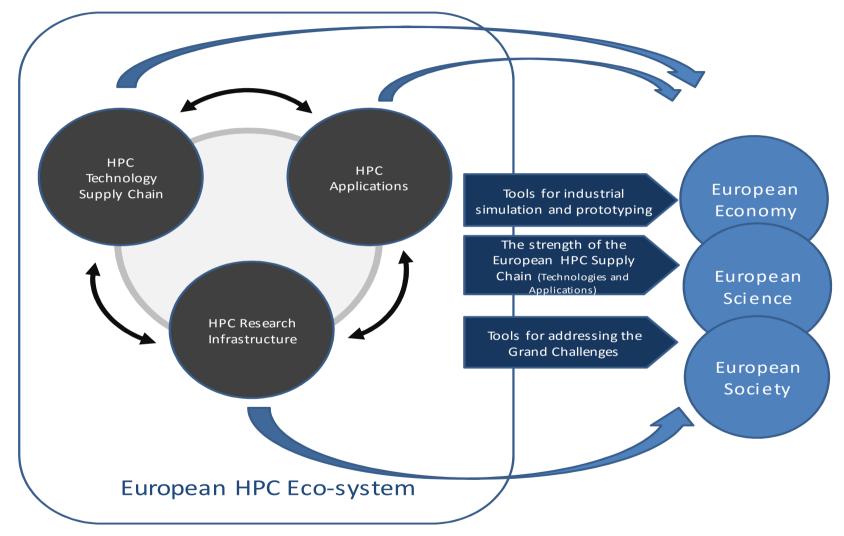


Main message:

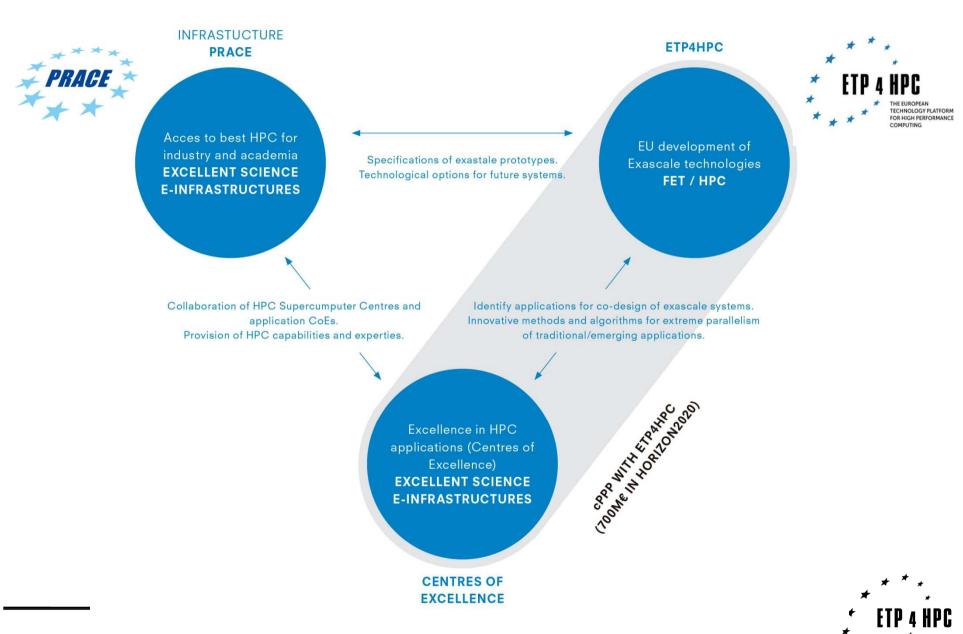
EU needs **independent** access to HPC technologies, systems and services



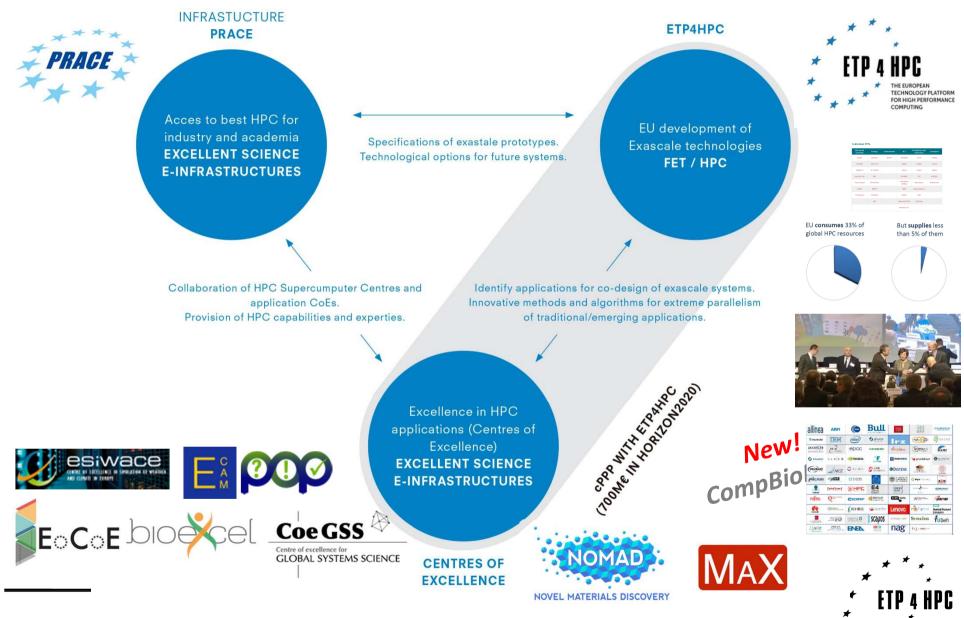






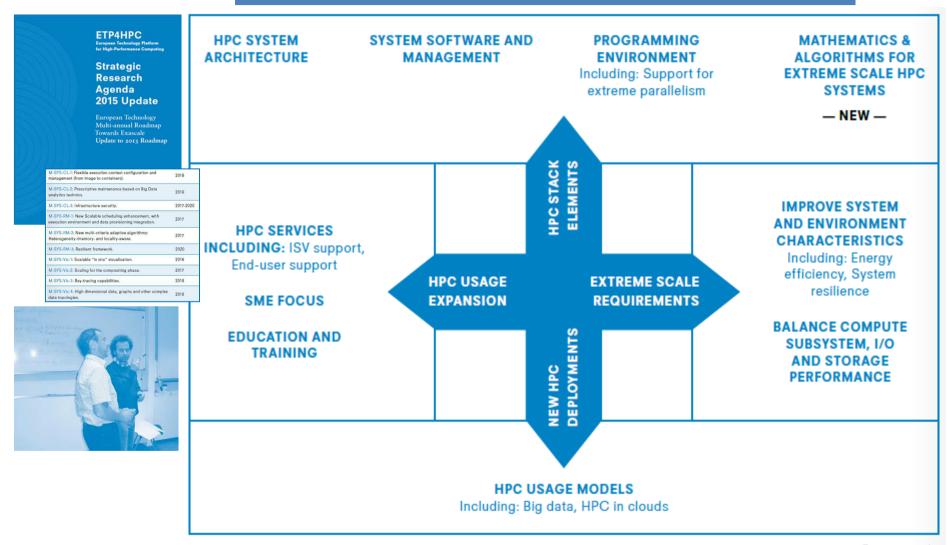


The European HPC Eco-system





Strategic Research Agenda

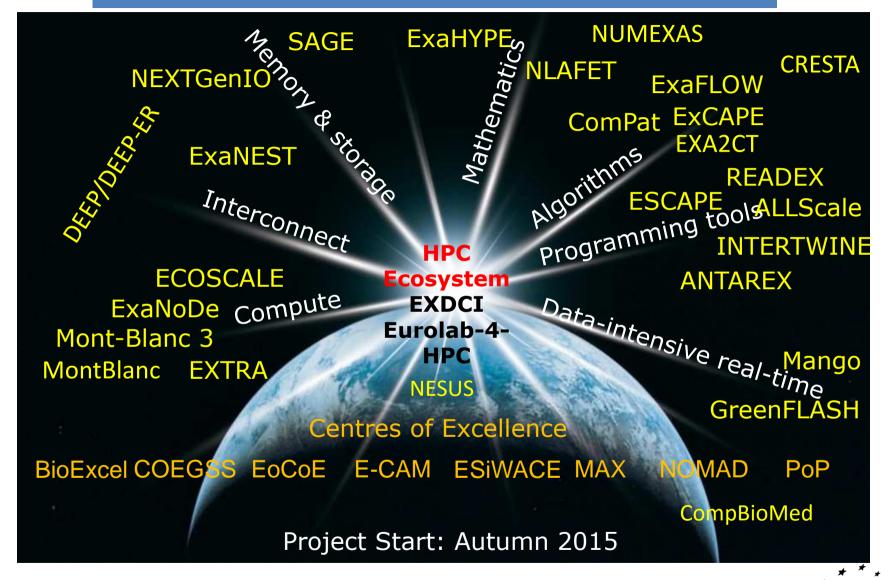




HPC — HORIZON 2020 ROADMAP 2013 2014 2015 2016 2017 2018 2019 2020 2021 2022 2023 WP 14/15 SRA 1 - Start of projects Start of call End of projects WP 16 Extreme Data SRA 2 & Computing Update Initiative FOR HIGH PERFORMANCE WP 17 Technology Development WP 18/19 Strategic Research SRA 3 Agenda 2015 Update WP 20/21 Co-Design SRA 3 SRA2 - Update **CSA 14/15: EXDCI** CSA 16/17: EXDCI 9/2015 2/2018 - 3/2018 8/2020



The EU HPC Technology Landscape



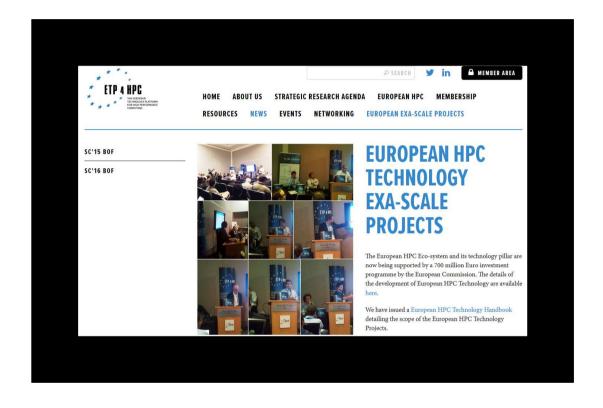


- Memory & Storage, Data-Intensive FETHPC project sample topics
 - Develop a new server architecture using next generation interconnection, and memory advances
 - Integration of NVRAM technologies in the I/O stack
 - Fast, distributed in-node non-volatile memory Storage
 - ../...
 - Develop the systemware to support new architectures use at the Exascale
 - Data Centric Computing System based on object-storage
 - .../...
 - Model different I/O workloads and use this understanding in a codesign process
 - Very Tightly Coupled Data & Computation
 - API for massive data ingest and extreme I/O
 - Extreme data management and analysis
 - /











How to involve the broad Big Data community in the SRA process?

We need feedback on the SRA from international experts – we have a Public Call for Comments open

We need your help in defining the **EsD system** requirements