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ETP4HPC EsD Workshop - 12th May 2016

Leveraging on Mont-Blanc project : ARM based compute nodes

Etienne Walter

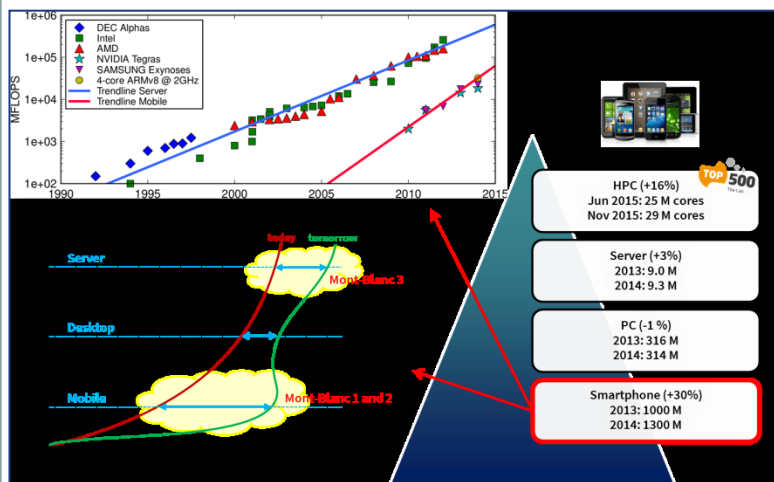
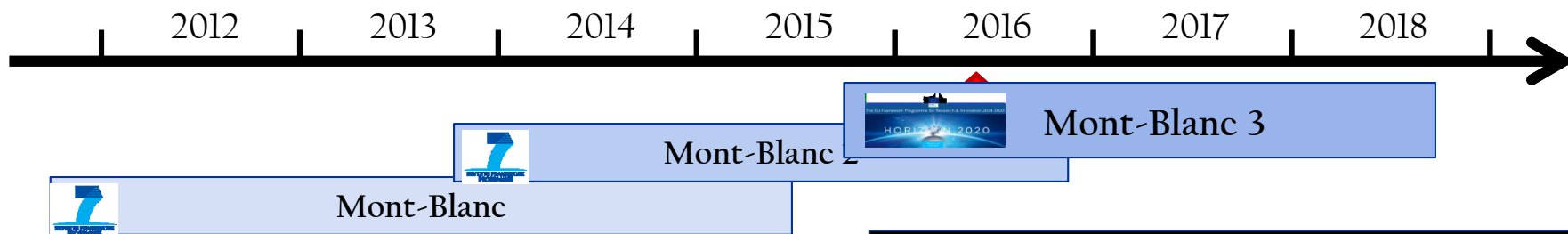
Project Manager at Bull/ATOS

Coordinator of the Mont-Blanc 3 project



Mont-blanc key take-away (in a nutshell)

Vision: to leverage the fast growing market of mobile technology for scientific computation, HPC and non-HPC workload.



- Mont-Blanc project (continuation) has the potential to bring the compute part of an EsD in 18-20 timeframe
(quite unclear now :
 - EsD timeframe & constraints ?
 - technology set ready in the right timescale ? ...)
- we (obviously) do not cover the whole scope of an EsD
- It is then a matter of
 - cooperation
 - technological choices (value added, maturity, complementarity ...)
 - our simulation work and models may help us in those choices
 - integration
- with a focus : an “user-ready” solution
(reliability, usability, completeness ...)
 - link with users & CoEs & get feedbacks

Mont-Blanc project



2-3 June 2016
Barcelona
<http://emit.tech/>



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“The secret is to win going as slowly as possible.”

Niki Lauda