



ETP4HPC SRA-3 kickoff meeting

IBM IOT, Munich, March 20th 2017

March 20, 2017



Agenda

- 10:30 Welcome and agenda review
- 10:45 New trends and requirements
 - 10:45-11:15 CoE: Peter Bauer
 - 11:15-11:45 EXDCI-WP3: Stephane Requena
 - 11:45-12:15 EuroLab-4-HPC: Theo Ungerer/Paul Carpenter
 - 12:15-12:45 HiPeac: Marc Durantou
 - 12:45-13:15 BDVA: Nenad Stojanovic
 - Working Lunch
 - 14:00-14:30 BDEC: Mark Asch
 - 14:30-15:00 IPCEI-roadmap (Jean Gonnord)
- 15:00-16:00 SRA-structure: what is new, what to change?
- 16:00-17:00 Process, next meetings, next steps

Some basic questions before writing the first word.....

- Since SRA 2 the HPC landscape in Europe changed :
 - European Open Science Cloud initiative
 - IPCEI
 - European Low-Power Microprocessor initiative
 - The SRA is assumed to take these initiatives into account to be a relevant document for the HPC stakeholders in Europe
 - Do we know enough about them ?
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- We need an open, in-depth interlock with the EC and IPCEI
 - We need to understand the technical roadmaps and the implementation plans

Should we take a different approach this time?

- SRA 1 and SRA 2:
 - (1) workgroup leaders and SRA team wrote the text and (2) workgroups contributed through additions, reviews, discussions.....
 - Should we gather more input from the working groups upfront?
 - We could collect topics and questions that we want to see covered in the document (see examples next page)
 - This might lead to a more cross-linked technical „chapter 5 (recommended research topics)“
 - This way the relevance of the document might increase as it brings more answers to questions of the ETP community

Subjects/topics and questions to be answered in the SRA (1)

Overall

- What happened with the HPC ecosystem in Europe since SRA 1 (2012) ?
 - HPC market dynamics, international view, awareness for HPC as driver for economical growth, impact to society and economical growth, changes in technology provider landscape in Europe, awareness at EC level, new initiatives
 - Acceptance of HPC as enabling tool in industry, investment outlooks
 - Assessment of European technology provider readiness and competitiveness
- How is the impact and role of HPC changing ?
 - Influences due to extreme data / HPDA , importance of „run to Exaflops“
- New players in the ecosystem
 - CoEs, ECDXI, Eurolab-4-HPC, BDVA, BDEC, etc

Subjects/topics and questions to be answered in the SRA (2)

Technical subjects

- Although the SRA is not a tutorial, we should explain new acronyms and define what we mean by :
 - Co-design, Difference between AI/ML/HPDA,
 - Metrics (FLOPS still so important ?, balanced architecture, new bechmark trends)
 - SDI, near memory execution....
 - AI supercomputers....

Roadmap

- **March 20th:** Kickoff meeting at IBM IOT center in Munich
(SRA –workgroup leads, application owners, CoE, HiPeac, BDVA, EUROLAB-4-HPC, BDEC)
- **March 27th:** New SRA working groups in place (based on WP18-20 workgroup participants)
- **April:** Gather input from working groups (questions /recommendations)
- **May 18th:** EsD - Roundtable during HPC summit (Barcelona)
(FETHPC projects, CoE, HPC centers, system integrators, technology providers)
- **May 19th:** SRA-workgroup leaders' internal meeting (Barcelona)
- **June 22nd:** Workshop at ISC with industrial users (“how to benefit from EsDs?”)
- **April-June:** writing, interlock with workgroups
- **July:** integration, tuning, review
- **July 31st:** completion target
- **September 15th:** release and distribution after WP17 call closure