

# The Strategic Research Agenda from 2014 to 2020

BDEC Wednesday, January 28<sup>th</sup>, 2015



# What is ETP4HPC?

- **EUROTECH** ARM Fraunhofer allinea 57 MAXELER (intel) **J**ÜLICH IBM INFN Street Section 1 C transtec scilab enterprises DISTENE epcc Ter@tec= SILKAN MICTON elijir FINMECCANICA Clustervision **O**SICOS ON INVIDIA. MEGWARE DataDirect ARCTUR FUÏTSU SYSFERA ICHEC
- European Technology Platform
  - Stakeholder representation
  - Dialog with European Commission
- ETP4HPC
  - Initiated in 2011
  - Open organization
- 64 organizations involved in HPC technology research based in Europe
  - 34 companies (20 SMEs) + 30 RTO
- Managed by a Board of 15 members



# Partnership for European leardership in HPC



## cPPP in a nutshell

## Mutual commitments

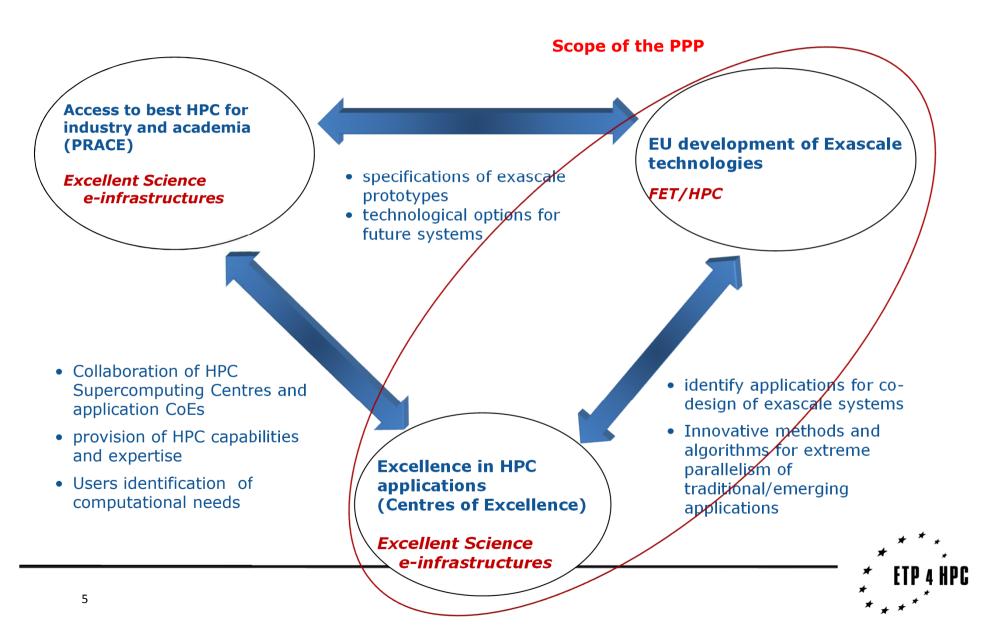
- EC: continuous support of HPC in Horizon 2020: 700 M€
- HPC community: R&D investment matching EC effort + industrial development

## Coordination of the action

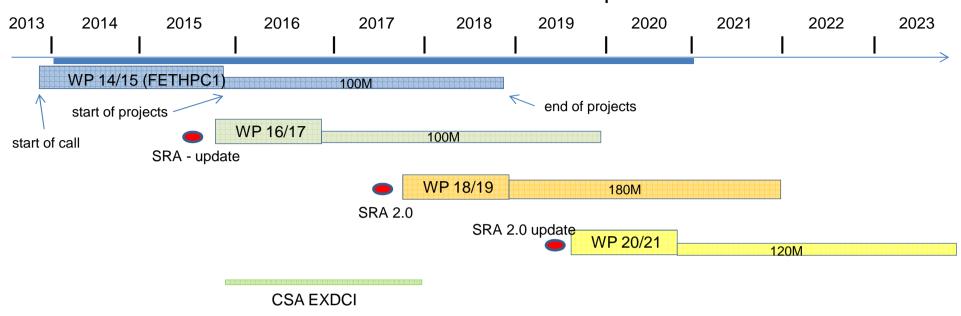
- Advices from stakeholders to EC
- Management of the work programmes by the EC
- Preparation of roadmaps proposing the vision
- Common monitoring of Key Progress Indicators



# Interrelation between the three elements



## HPC-Horizon 2020 roadmap



### FETHPC /CSA in WP16/17

EsD set 1

**Extreme-scale HPC Demonstrators:** 

EsD set 2

#### **Extreme-scale HPC Demonstrators integration/validation:**

- Non commercial, pre-Exascale HPC systems
- Technical readiness level at integration-phase-exit: 8
- Scoping starts in 2015 (top down: problem to solve, applications, system architecture, technology)
- Integration, built and test funded through dedicated calls in 2017/18
- Target use: deployed by PRACE and CoEs for appl. dev. / tuning /benchmarking
- Demonstrate and prove effectiveness of HPC research projects



## The HPC research focus areas for 2016/17

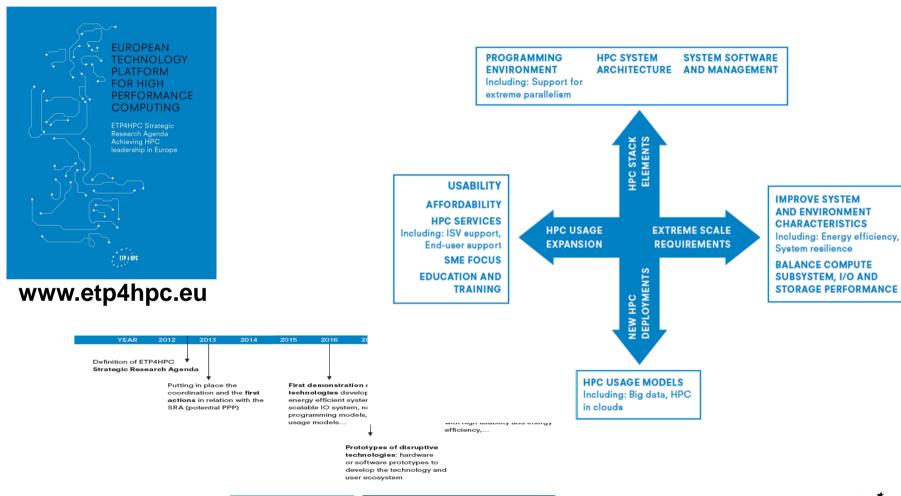
## Research focus areas

- Co-design of HPC systems and applications
- Focused research projects
  - High productivity programming environments
  - System software and management
  - > Exascale I/O and storage in the presence of multiple tiers of data storage
  - Supercomputing for Extreme Data and emerging HPC modes
  - Mathematics and algorithms for extreme scale HPC systems and applications working with extreme data

HPC ecosystem development and international collaboration CSA



# Strategic Research Agenda: a multi-dimensional vision



Second phase of the European

First phase of the European HPC



## SRA update methodology

- One of the activity of EXDCI-CSA
- > SRA-2 in 10/2015 as update (addedum) to SRA-1 released in 2013
- > SRA-3 in 10/2017 will be a bottom-up new release again
- Sources of input:
  - technical experts from ETP4HPC-members, EESI, PRACE, SKA, BDV (+ expanding network)
  - HPC users (academia and industry), ISVs
  - information out of international collaborations
- Four research focus areas
  - HPC system architecture and design for extreme scale
  - High productivity programming environment and system software
  - Exascale I/O and storage, Supercomputing for Extreme Data and emerging use modes
  - Scoping of Extreme Scale Demonstrators
- Reviewed within HPC community before release



# International collaboration

- Develop synergies with the most active areas
  - Analyze the complementary of R&I programs
  - Identify common effort on Exascale software stack and programing models
  - Exchange experience in HPC application development
  - Share vision and determine if joint communication is needed
  - EC could establish MoU's with MEXT and DoE
- Collaborate with some of the countries developing their HPC strategies
  - Joint call on some strategic topics for the partner
  - Explore the experience developed by the CoEs
  - Potential partners : Algeria, Brazil, Mexico, Morocco, South Africa





# Question?





# **THANK YOU!**

For more information visit

www.etp4hpc.eu

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