

ESD ROUNDTABLE AT EUROPEAN HPC SUMMIT WEEK 2017

FETHPC project presentations should help to understand:

One page teaser format:

- Highlights of your project (5-8 lines)
- What are anticipated technology (hw/sw/methodology) suggested for inclusion in an EsD project and describe the current maturity?
- How should this technology be used / integrated (I/F, APIs)
- Are there any pre- or co-requisite items
- Any extra work/interaction (on top of current project roadmap) needed to make them ready?
- What information / actions are needed to best prepare for EsD projects?