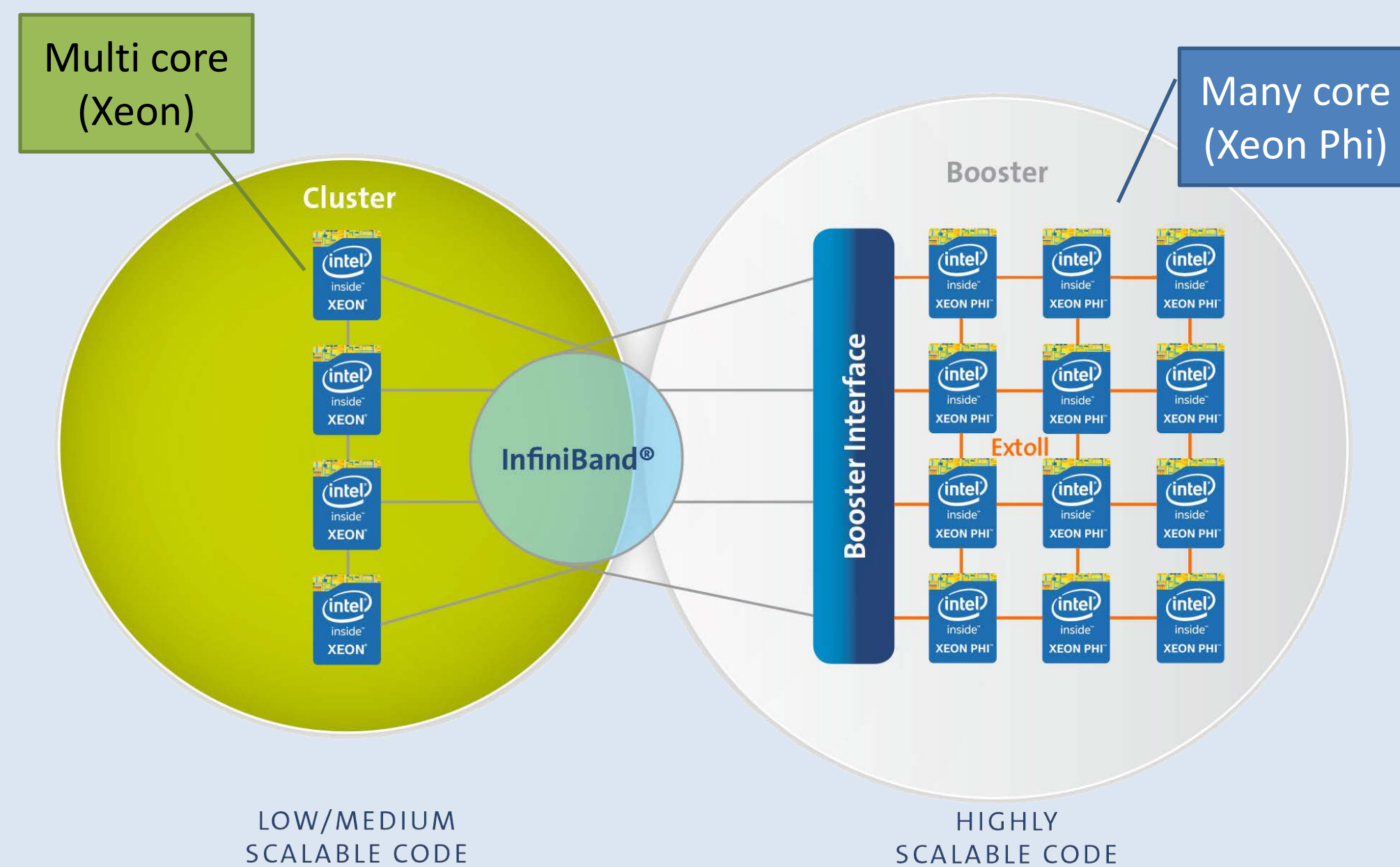


Cluster-Booster Architecture:

- General-purpose **Cluster** + Many-core **Booster**
- Run applications on best suited hardware

1st Generation: DEEP



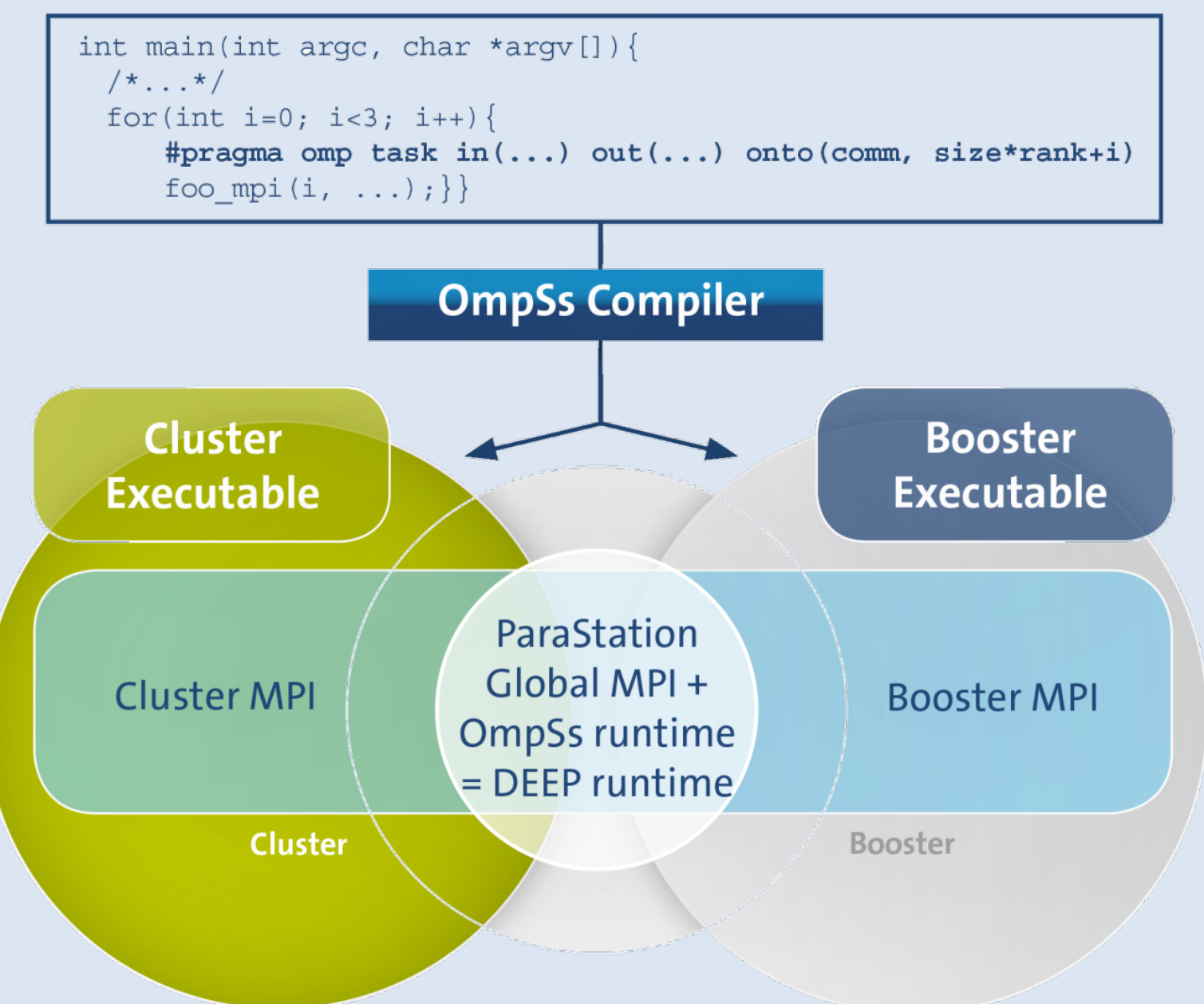
DEEP Prototype



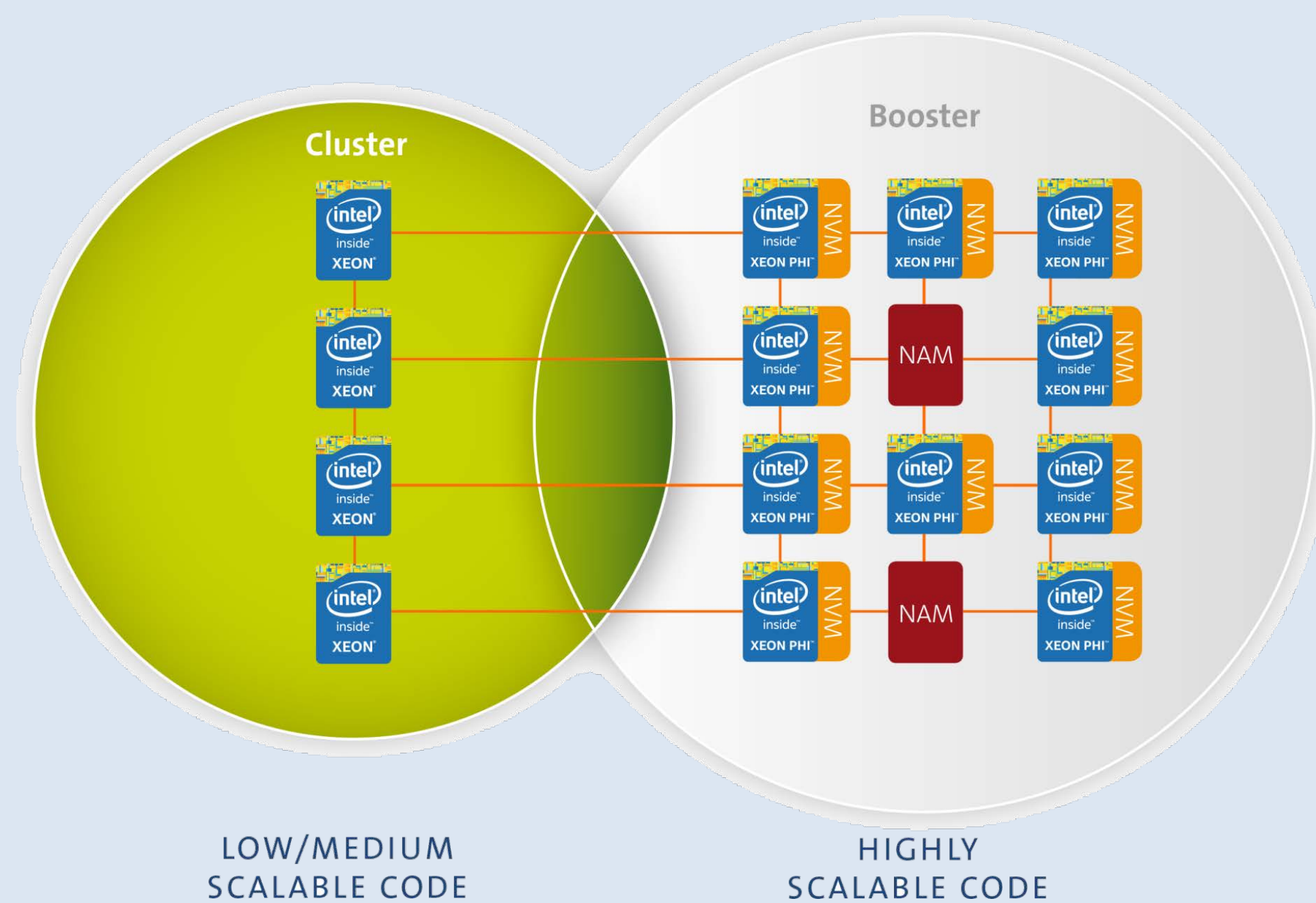
500 TFLOP/s
Peak Performance

System SW

- Global MPI + task-based parallel offload



2nd Generation: DEEP-ER



I/O and resiliency: DEEP-ER

- KNL Booster nodes
- NVM-based memory hierarchy
- Software enhanced with fast, highly scalable I/O and resiliency systems

Driven by Co-Design:

- Diverse, real-world HPC applications

